

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1875.4360004APPLICATION NO.
10/697,286INVENTORS
CATALASAN *et al.*FILING DATE
October 31, 2003ART UNIT
2811

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
SWC	AA1	5,590,069	12/1996	Levin			
	AB1	5,644,144	07/1997	Ray			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL						Yes
	AM						No
	AN						Yes
	AO						No
	AP						Yes
							No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

SWC	AR	1	Catalasan <i>et al.</i> , U.S. Patent Appl. No. 10/697,079, filed October 31, 2003, entitled "Memory Cell For Modification Of Revision Identifier In An Integrated Circuit Chip," 54 pages.
SWC	AS	1	Catalasan <i>et al.</i> , U.S. Patent Appl. No. 10/697,889, filed October 31, 2003, entitled "Method For Manufacturing A Memory Cell For Modification Of Revision Identifier In An Integrated Circuit Chip," 53 pages.
SWC	AT	1	Catalasan <i>et al.</i> , U.S. Patent Appl. No. 10/697,289, filed October 31, 2003, entitled "Coupling Of Signals Between Adjacent Functional Blocks In An Integrated Circuit Chip," 77 pages.

EXAMINER

CRANE

DATE CONSIDERED

9/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.